T1 T2 T3 T4 T5 1Q

cut

cut

Ignore

cut

5ns delay per tap

Connected 1D input to 2D input

D0

2Q

1D

TI

GND

PWR

100 ohm

0 ohm jumper

decoupling capacitors

Demuxd signal output (DO2)
CLK used to trigger flipflop
DO2 received from driver

Demuxed signal output (DO1)
CLK, CMD, NTC not used on board
DO1 received from driver

Muxed signal input
DO sent to receiver, coverts from LVDS to TTL
DOTTL triggered on flipflop for both channels